WHAT IS CLAIMED:

1. An atomic layer deposition (ALD) process for producing a thin multicomponent oxide film on a substrate, the thin multicomponent oxide film comprising silicon, transitional metal, and oxygen, the process comprising:

contacting a substrate in a reactor with a vapor phase silicon compound such that the silicon bonds to the substrate;

contacting the substrate with a vapor phase metal compound such that the metal bonds to the substrate;

converting the bonded silicon and metal compounds into an oxide by contacting them with a reactive vapor phase oxygen source; and

purging the reactor with an inert gas after each contacting step and after each converting step.

- 2. The process of Claim 1, wherein the process is repeated to form a layer of a desired thickness.
- 3. The process of Claim 1, wherein the oxygen source compound is selected from the group consisting of water, oxygen, ozone, and hydrogen peroxide.
 - 4. The process of Claim 1, wherein the metal compound is a metal halide.
 - 5. The process of Claim 4, wherein the metal compound is hafnium tetrachloride.
 - 6. The process of Claim 1, wherein the silicon compound is a silicon halide.
- 7. The process of Claim 1, wherein the silicon compound is selected from the group consisting of silicon tetrachloride, hexachlorodisilane, and hexachlorodisiloxane.
- 8. The process of Claim 1, wherein the deposition occurs at a temperature range of between 150°C and 450°C.
- 9. The process of Claim 1, wherein the deposition occurs at a temperature range of between 300°C and 350°C.
- 10. The process of Claim 1, wherein the thin multicomponent oxide film is formed on a hemispherical grain structure.
 - 11. The process of Claim 1, wherein the substrate is a grooved flat material.
 - 12. The process of Claim 1, wherein the substrate is a flat material.

- 13. The process of Claim 1, wherein the substrate is a bottom electrode of a Dynamic Random Access Memory capacitor.
- 14. The process of Claim 1, further comprising depositing a high dielectric constant material over the thin multicomponent oxide film.
- 15. The process of Claim 14, wherein the high dielectric constant material is an oxide of the metal in the metal compound.
- 16. The process of Claim 1, wherein the thin multicomponent oxide film is deposited on a silicon interface to form part of a transistor gate dielectric.
- 17. The process of Claim 16, further comprising depositing a high dielectric constant material over the thin multicomponent oxide film.
- 18. The process of Claim 1, wherein the thin multicomponent oxide film forms an interlayer in a transistor gate oxide.
- 19. The process of Claim 1, wherein a ratio of silicon compound contacting steps to metal compound contacting steps during the ALD process is in the range of one to ten and ten to one.
- 20. The process of Claim 19, wherein the ratio of silicon compound contacting steps to metal compound contacting steps during the ALD process is one to one.
- 21. The process of Claim 1, wherein converting comprises separate oxidation steps following each of the contacting steps.
- 22. An atomic layer deposition (ALD) process for producing a thin oxide film comprising transitional metal, and oxide on a substrate, the process comprising:

pulsing a vapor phase silicon compound into a chamber such that the silicon bonds to the substrate;

pulsing a first reactive vapor phase oxygen source into the chamber to convert the bonded silicon compound into an oxide by contacting them with a reactive vapor phase oxygen source;

pulsing a vapor phase metal compound into the chamber such that the metal bonds to the substrate;

pulsing a second reactive vapor phase oxygen source into the chamber to convert the bonded metal compound into an oxide; and

purging the reactor with an inert gas after each pulsing.

- 23. The process of Claim 22, wherein the first oxygen source is the same as the second oxygen source.
 - 24. A method of manufacturing a gate dielectric film on a substrate comprising adsorbing a layer of a silicon compound on the substrate in a self-limiting reaction;

adsorbing a layer of a metal compound on the substrate in a self-limiting reaction; converting the adsorbed silicon and metal compounds into a tertiary oxide by contact with a reactive vapor phase oxygen source compound; and

purging the reactor with an inert gas after each contacting step and after each converting step.

- 25. The method of Claim 24, further comprising repeating adsorbing and converting to form a layer of a desired thickness.
- 26. The method of Claim 24, wherein the oxygen source compound is selected from the group consisting of water, oxygen, ozone, and hydrogen peroxide
 - 27. The method of Claim 24, wherein the metal compound is a metal halide.
 - 28. The method of Claim 24, wherein the metal compound is hafnium tetrachloride.
 - 29. The method of Claim 24, wherein the silicon compound a silicon halide.
- 30. The method of Claim 24, wherein the silicon compound is selected from the group consisting of silicon tetrachloride, hexachlorodisilane, and hexachlorodisiloxane.
- 31. The method of Claim 24, wherein the silicon compound is converted into an oxide by contact with a reactive vapor phase oxygen source before the introduction of the metal compound.
- 32. The method of Claim 24, wherein the deposition occurs at a temperature range of between 150°C and 450°C.
- 33. The method of Claim 24, wherein the deposition occurs at a temperature range of between 300°C and 350°C.